



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: G.R. Mohan Rao

Serial No.: 10/665,906

Filing Date: September 18, 2003

Art Unit: 2818

Title: MEMORIES FOR ELECTRONIC SYSTEMS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Applicant hereby submits the following references in accordance with 37 C.F.R. §§ 1.56, 1.97 and 1.98. Copies of the references cited in the attached PTO/SB/08A are not enclosed nor required. Furthermore, pursuant to 37 C.F.R. § 1.97(g) and (h), no representation is made that this is material to patentability of the present application or that a search has been made.

Applicant hereby submits that claims of Applicant's above-referenced patent application are patentably distinguishable from these references.

ATTORNEY DOCKET NO.  
17200-P043US

PATENT  
10/665,906

It is believed that no fees are due; however, the Director of Patents and Trademarks is hereby authorized to charge any fees relating to this Information Disclosure Statement under 37 CFR § 1.17 to Deposit Account No 23-2426 of WINSTEAD SECHREST & MINICK P.C.

Respectfully submitted,



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Ruben C. DeLeon  
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Date: June 21, 2005

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17200-P043US 6/14/2005



PTO/SB/08A (04-03)

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Application Number	10/665,906
Filing Date	September 18, 2003
First Named Inventor	G.R. Mohan Rao
Art Unit	2818
Examiner Name	

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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Attorney Docket Number 17200-P043US

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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